

Figure 1 (Prior Art)

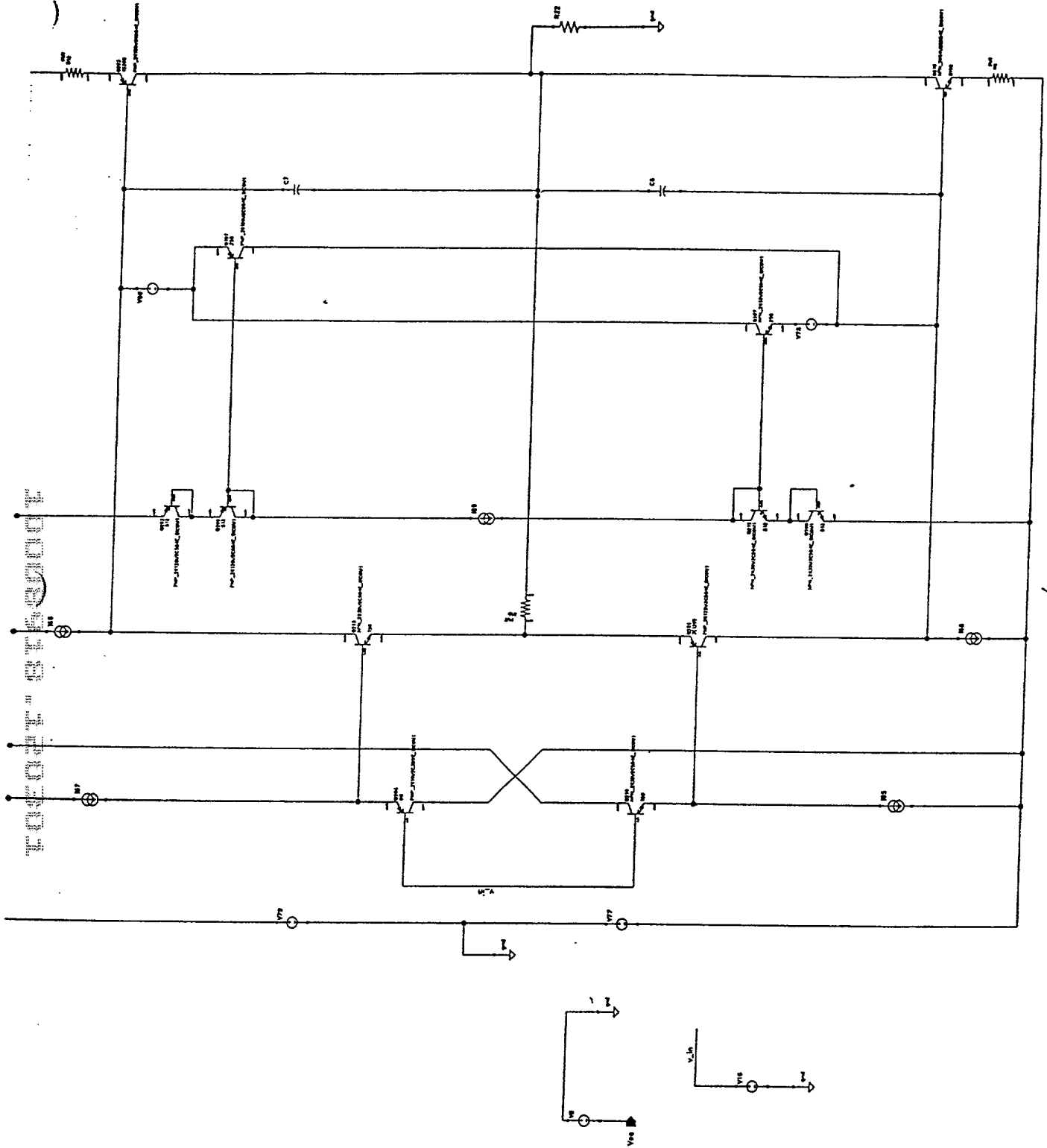


Figure 2 (Prior Art)

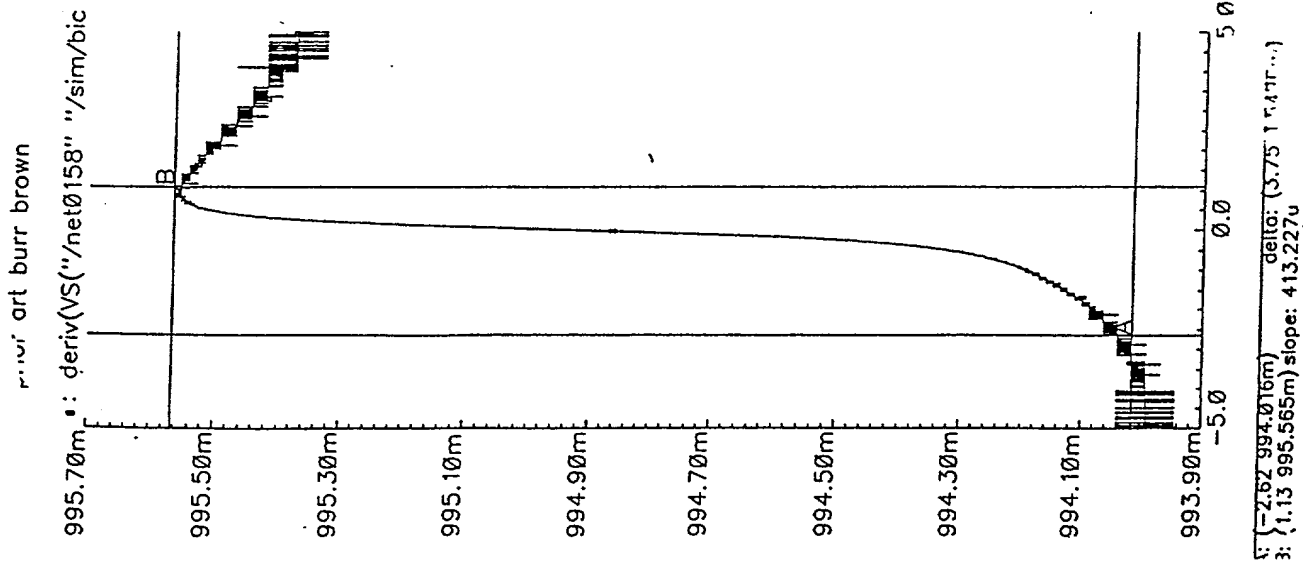


Figure 3 (Prior Art)

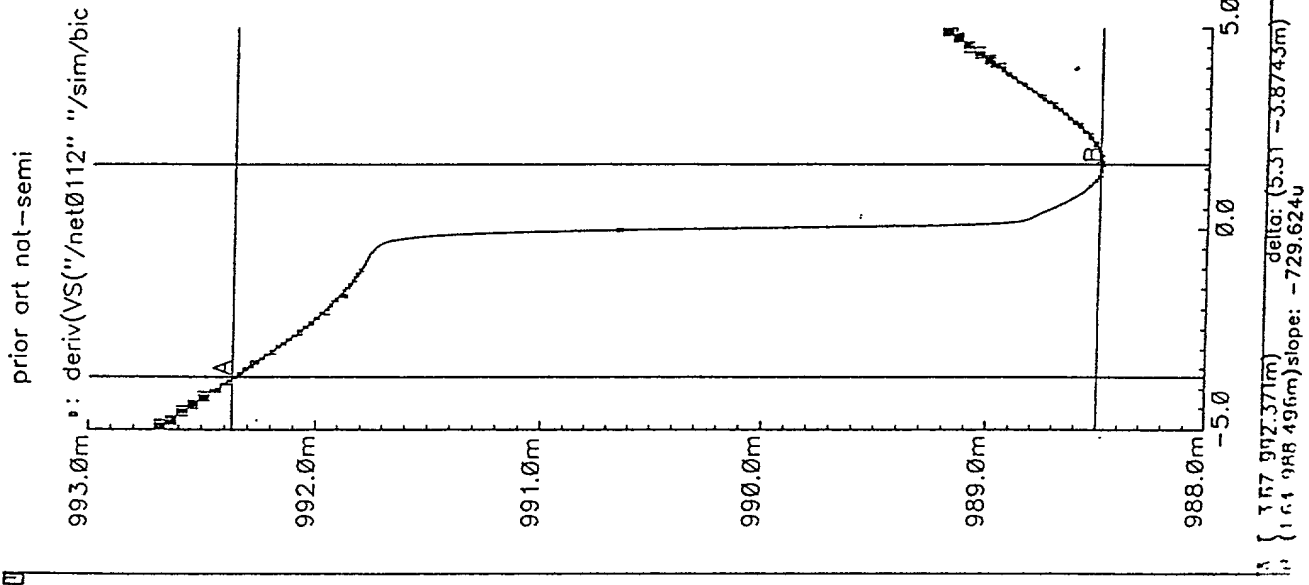


Figure 4 (Prior Art)

prior Art **burg-brown**

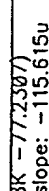
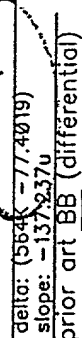


Figure 5 (Prior Art)

std op stage

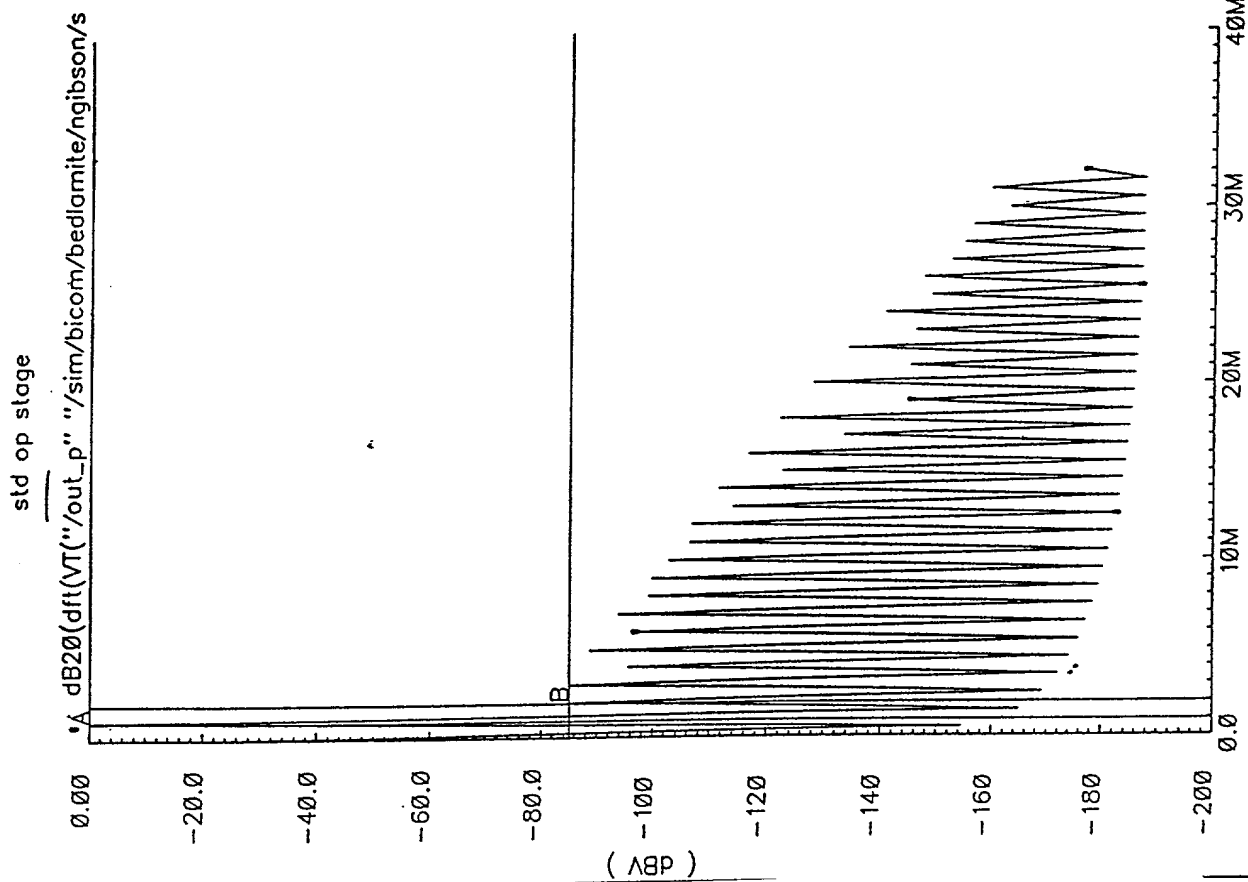


Figure 6 (Prior Art)

4003593 40034

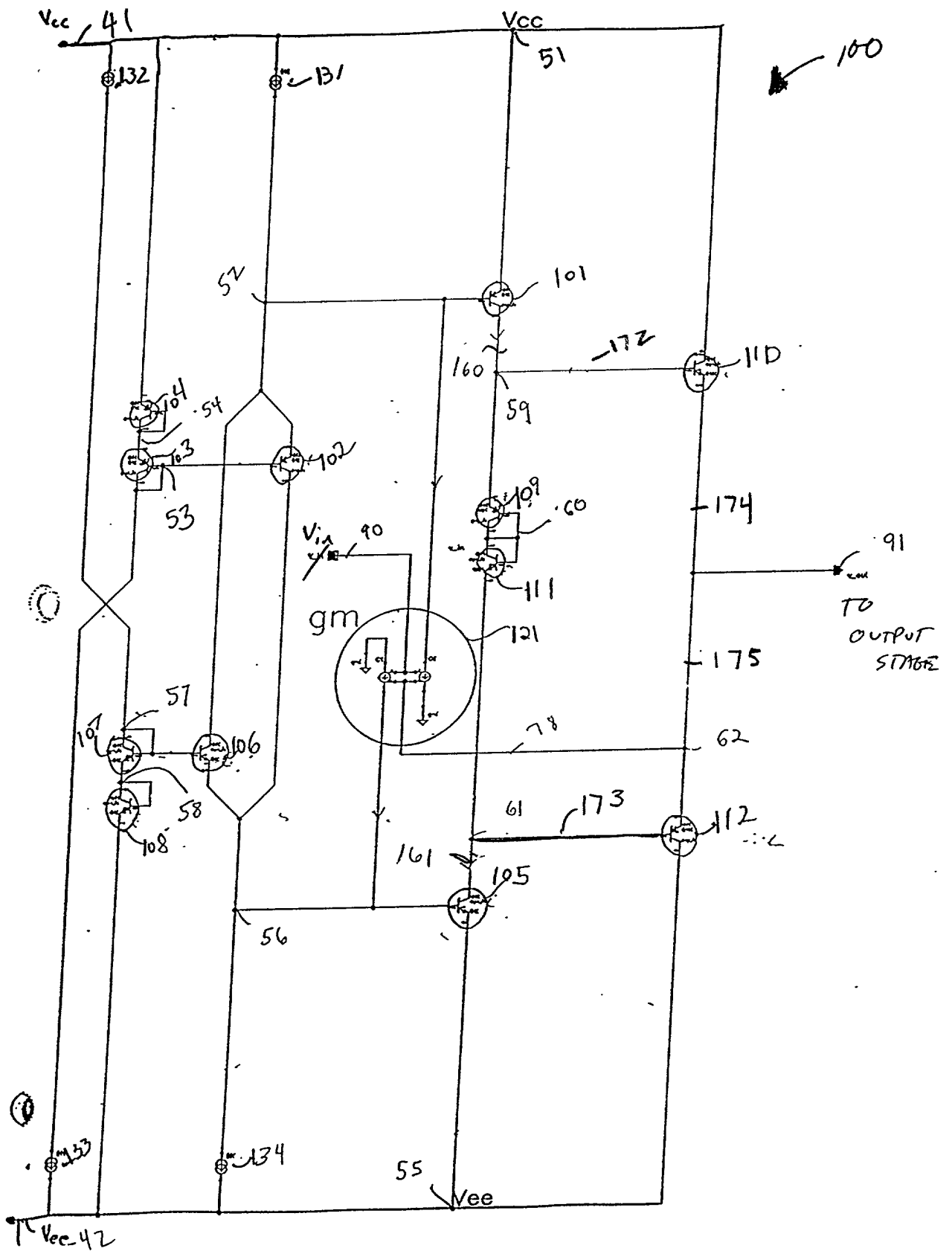


Figure 7

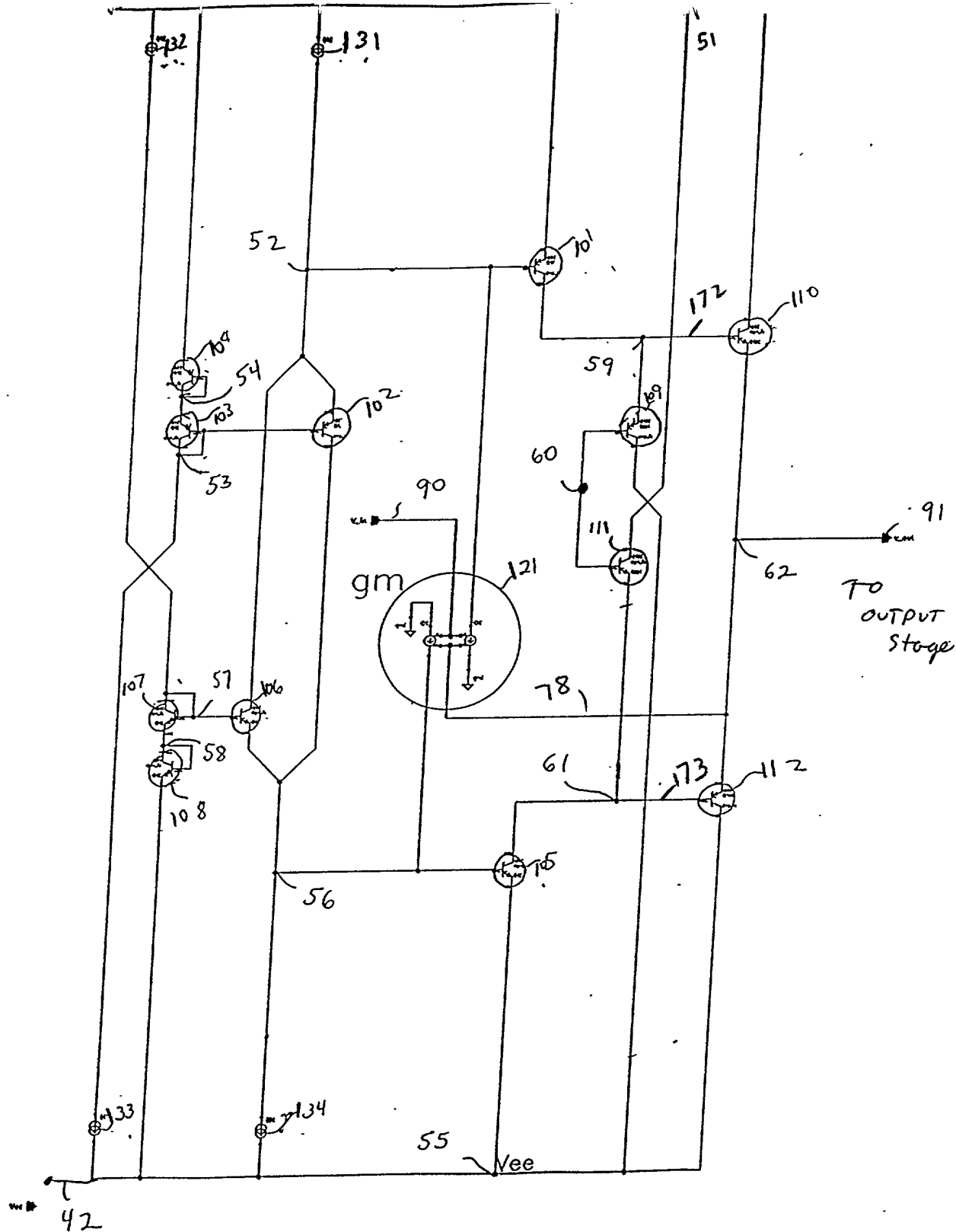


Figure 8

Plot of 345000

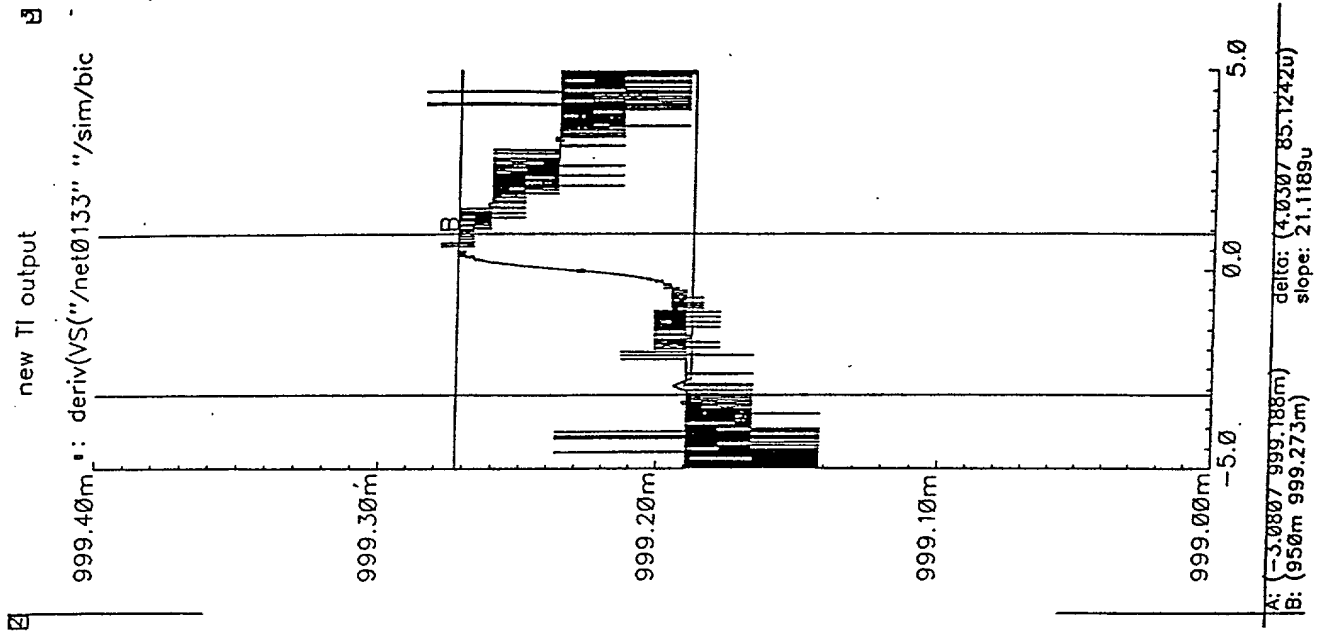


Figure 9

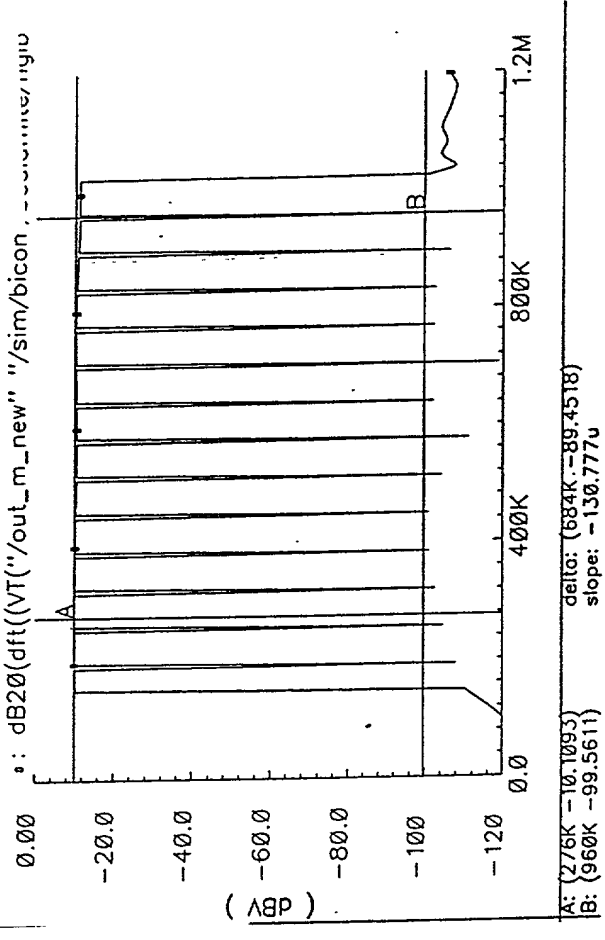
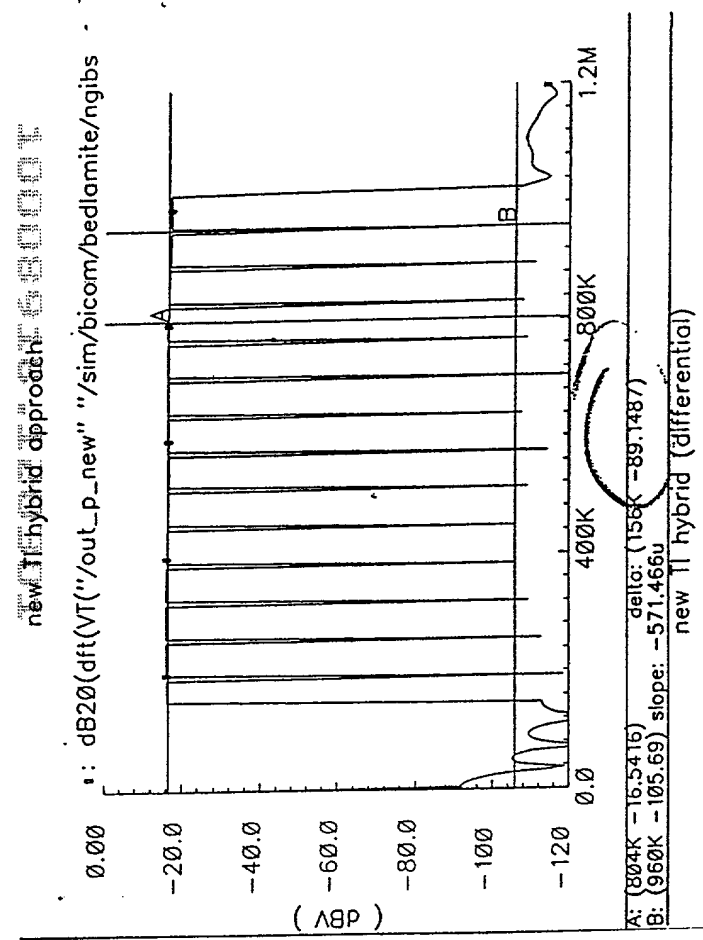


Figure 10

Plot of 315000
new up stage

dB20(dft(VT("/out_p" "/sim/bicom/bedlamite/ngibson/s

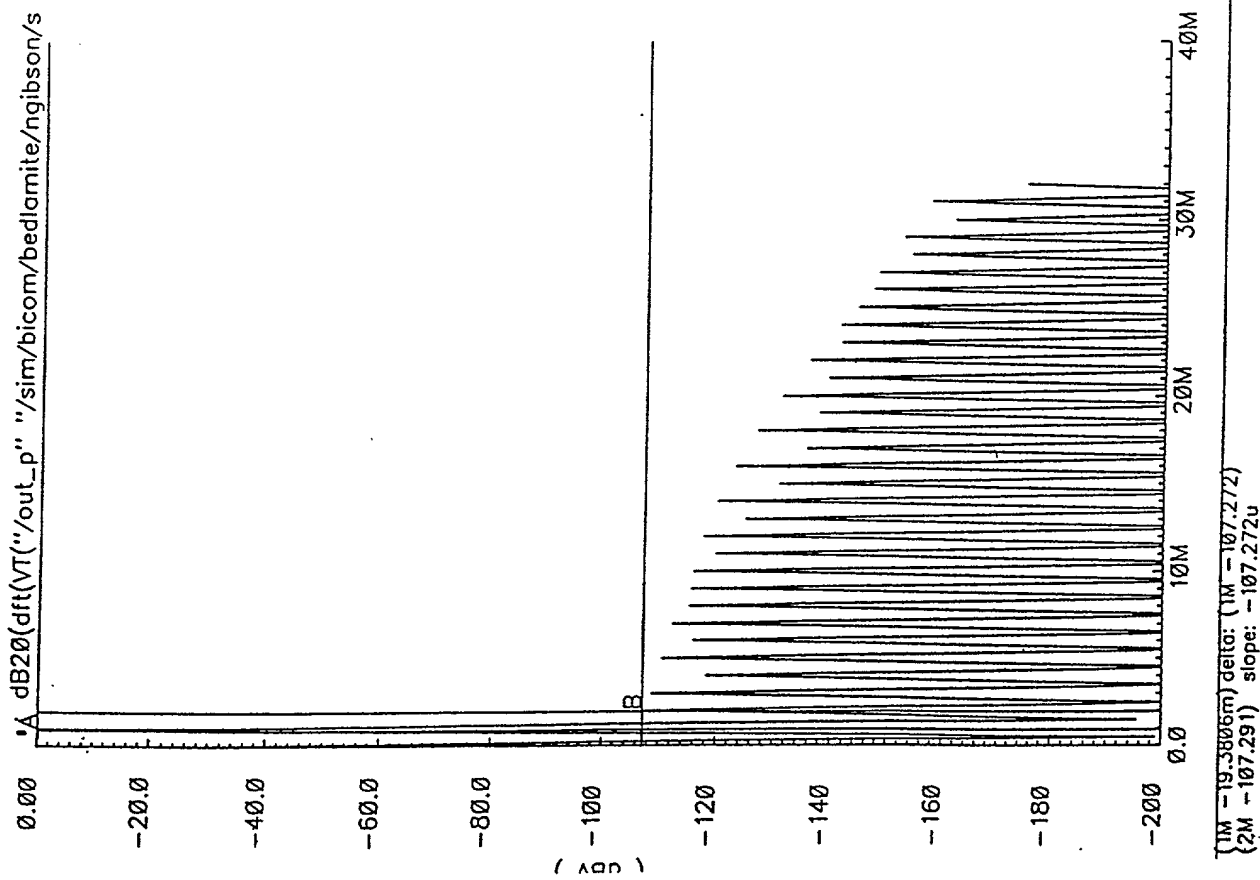


Figure 11

4000593 40004

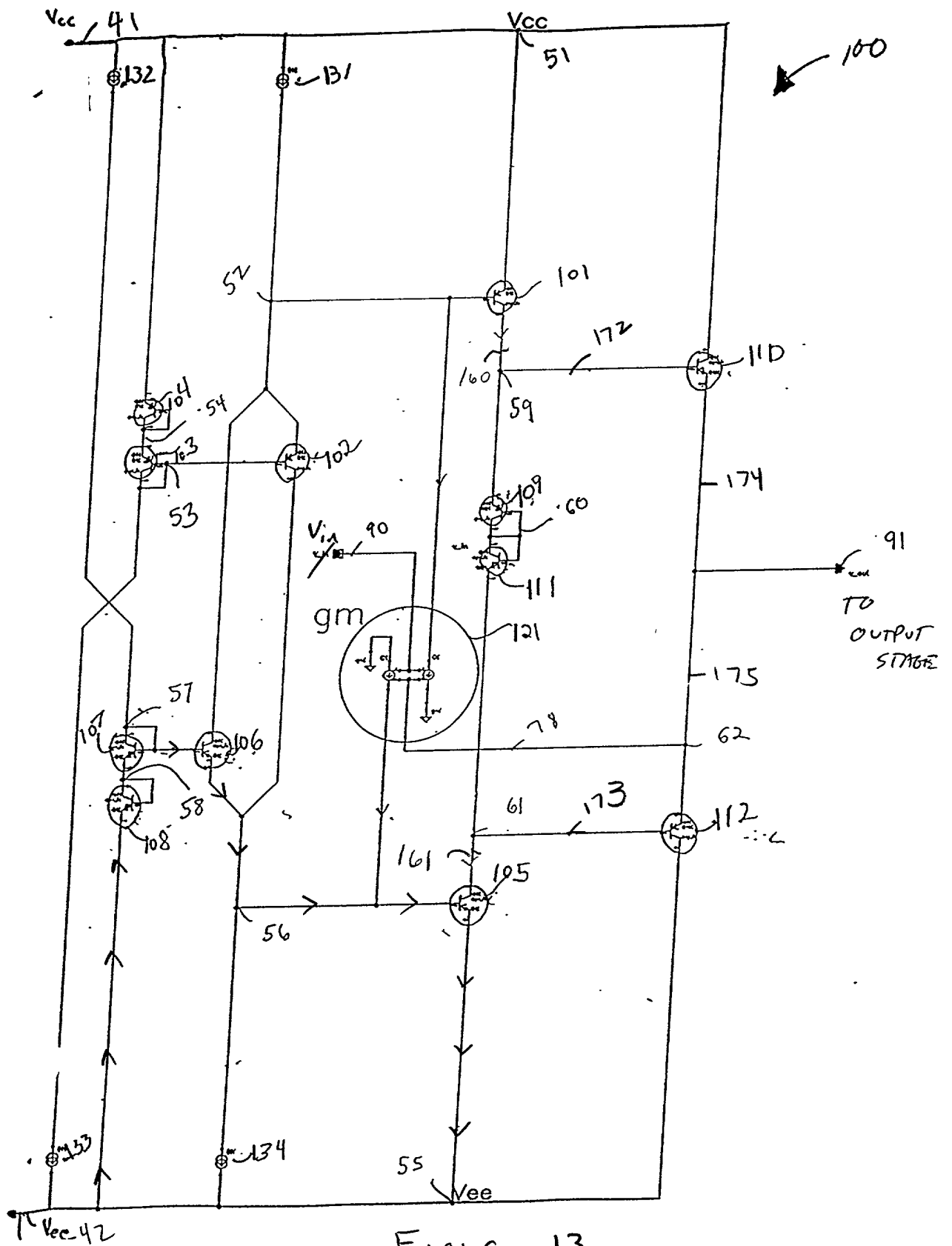


Figure 13

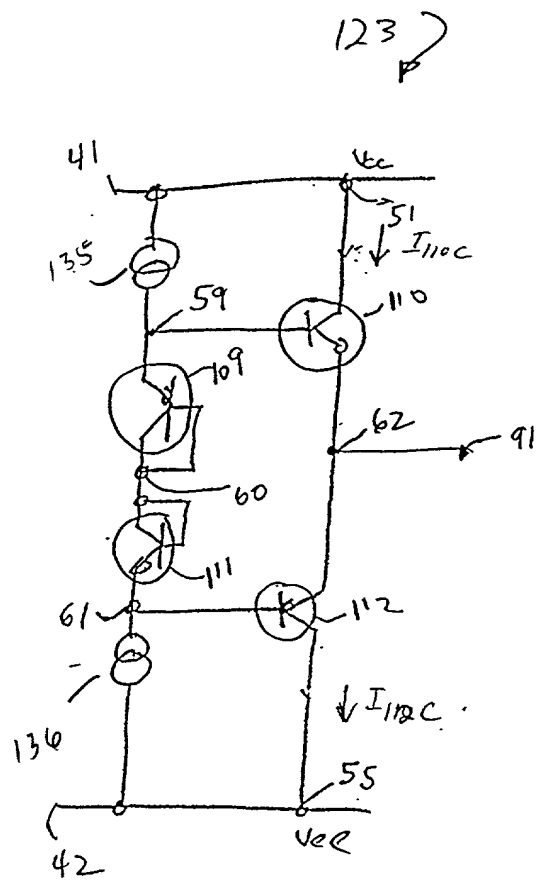


Figure 14

123)

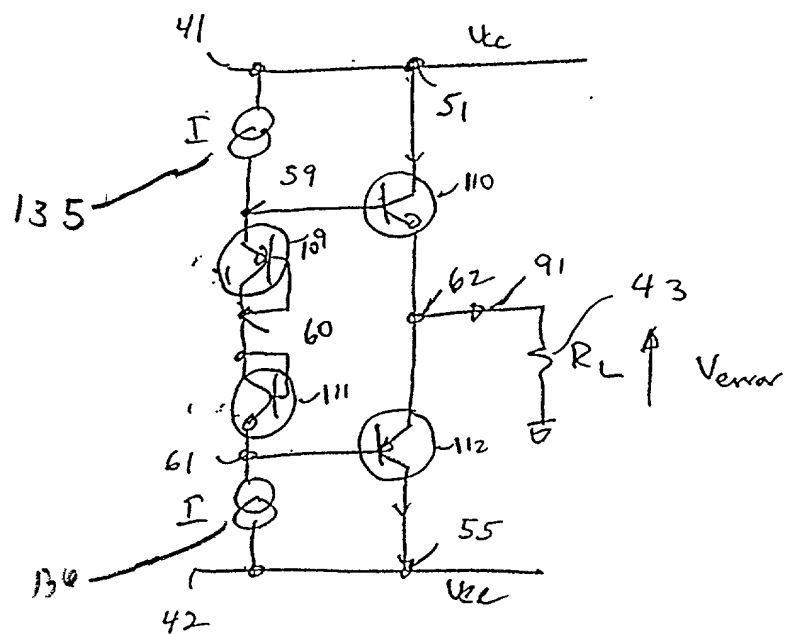


Figure 15

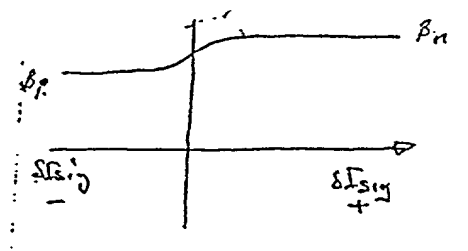


Figure 16

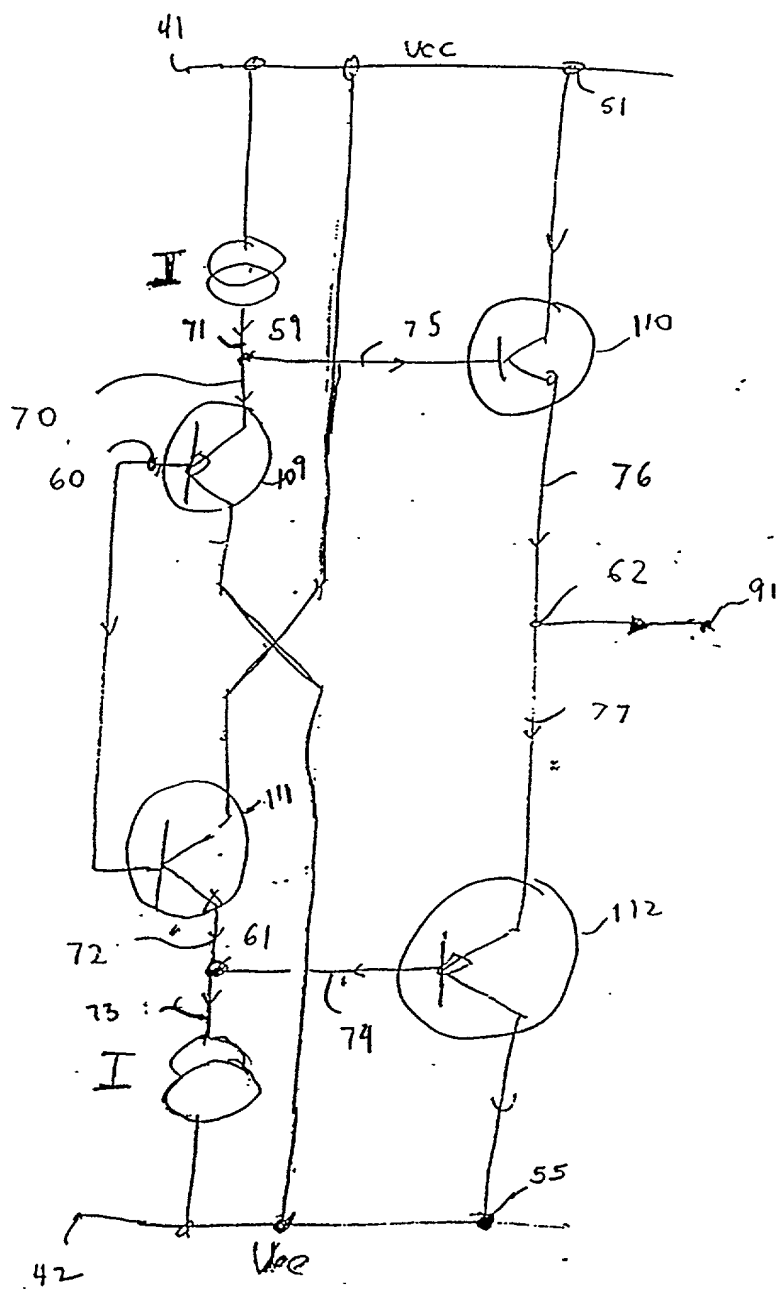


Figure 17

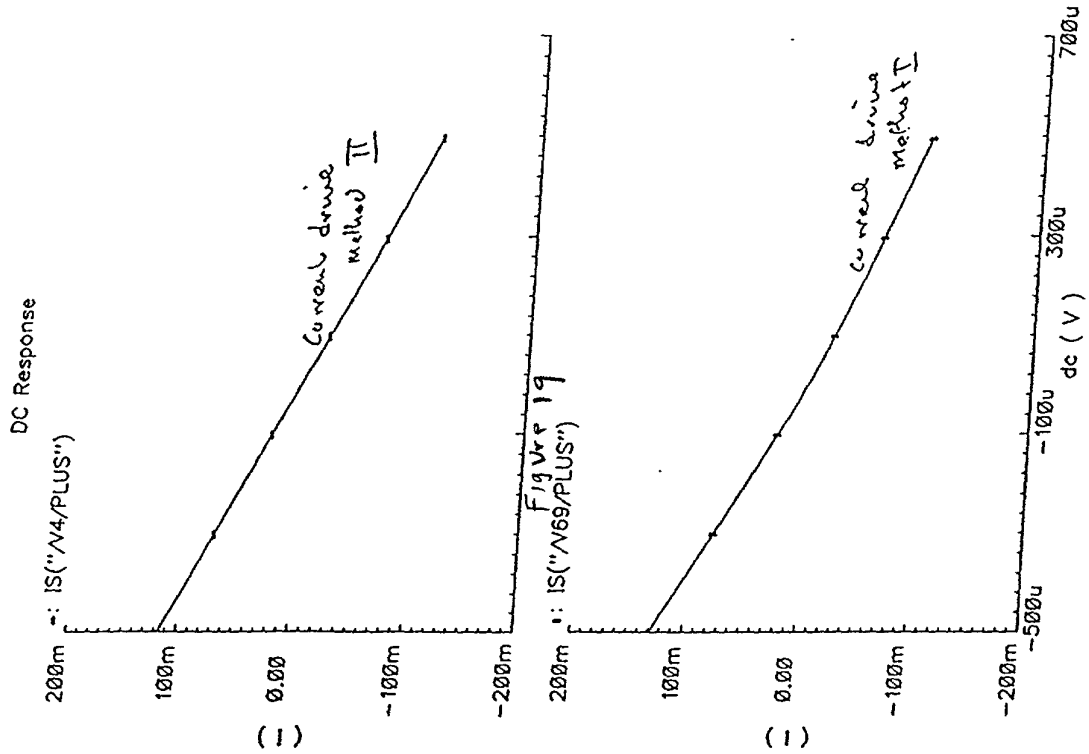
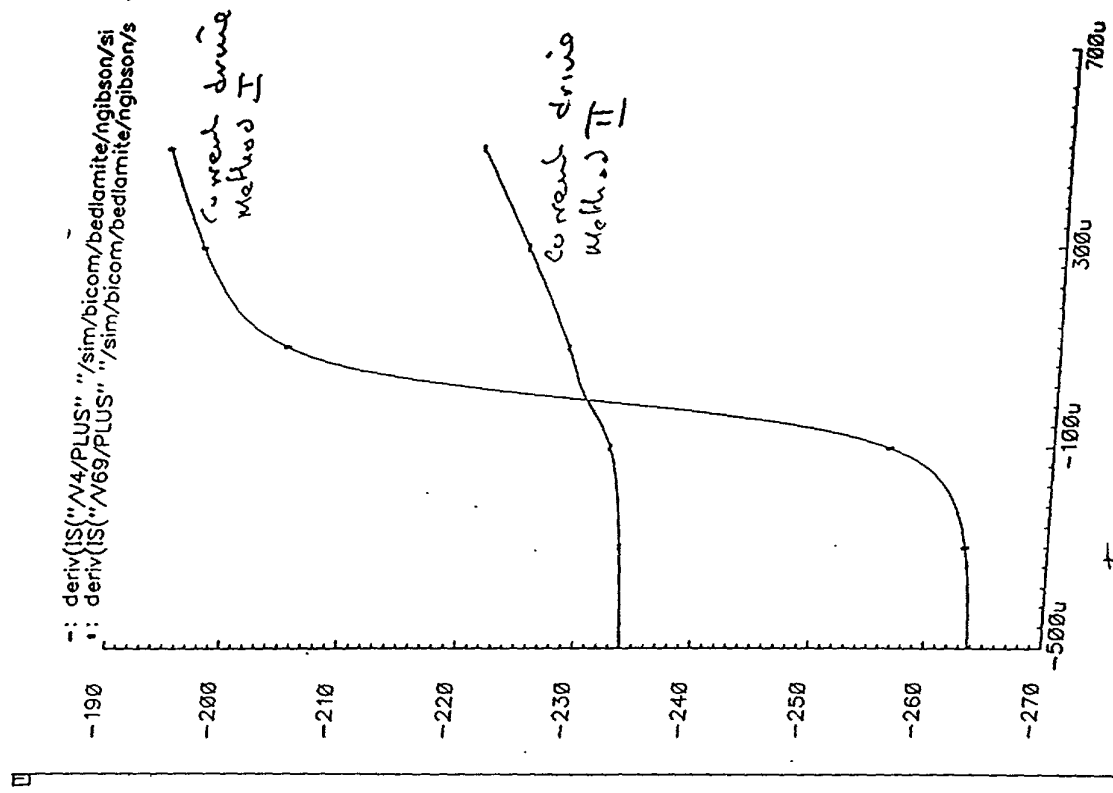


Fig - 19



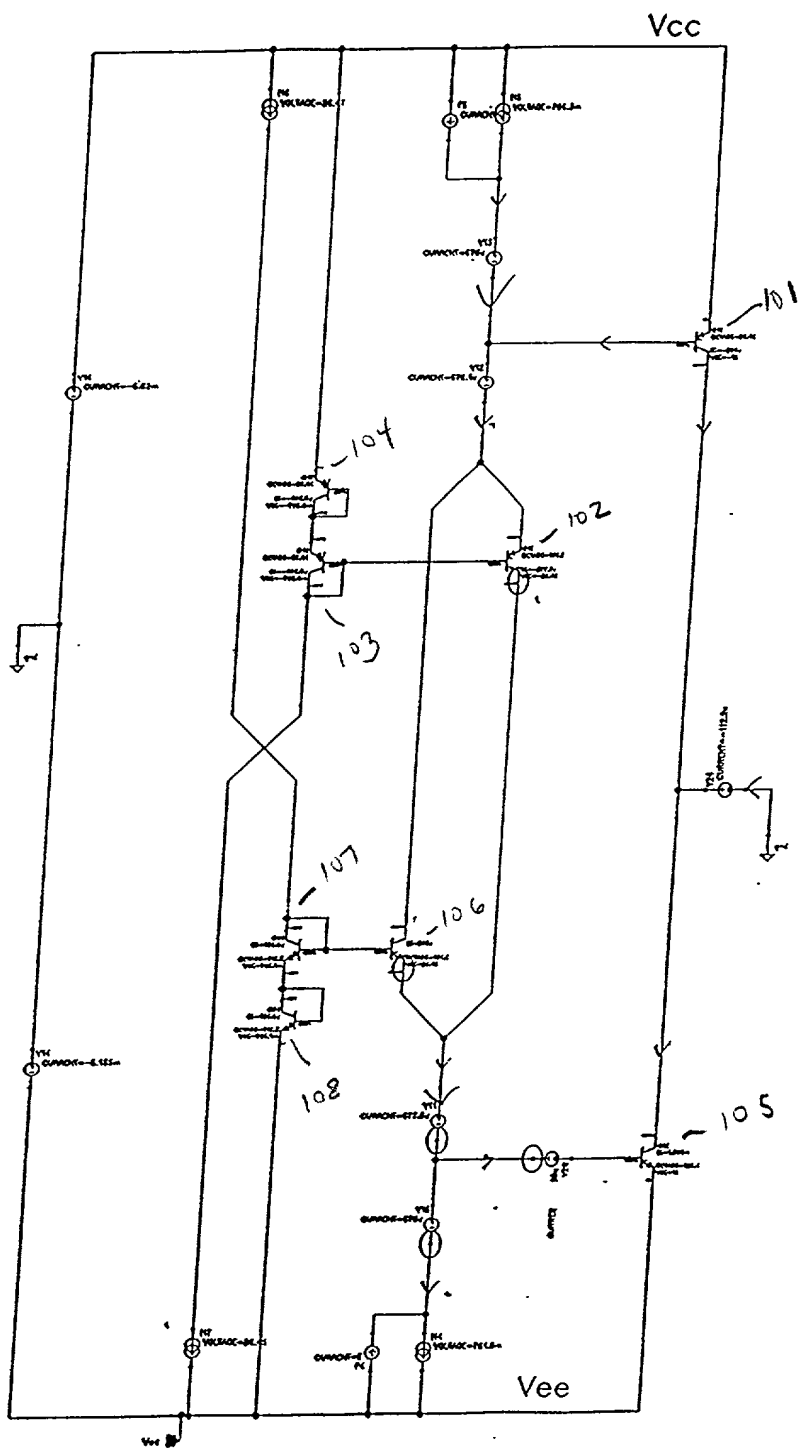


Figure 22

DC Response

14

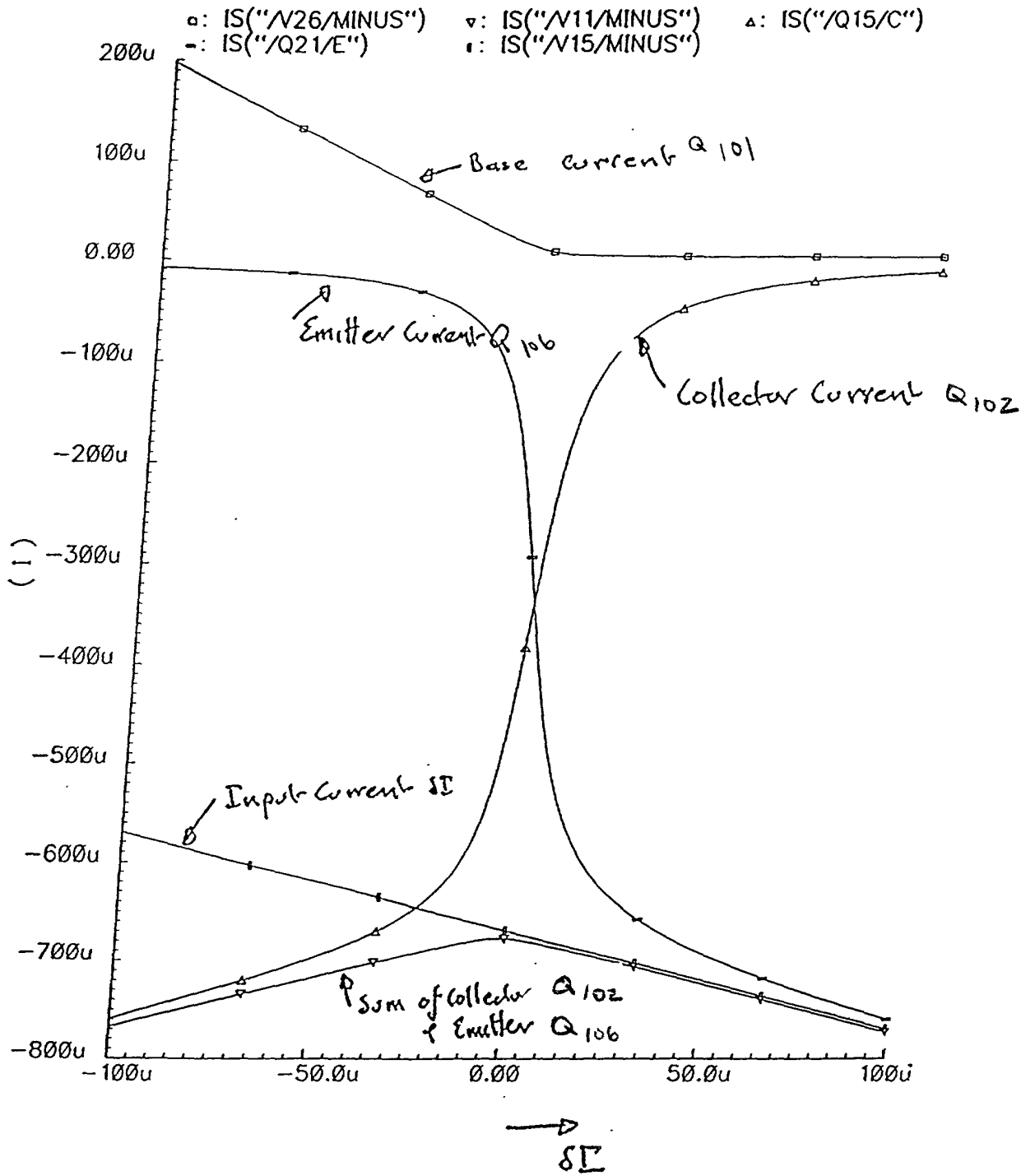


Figure 23

60033-4003

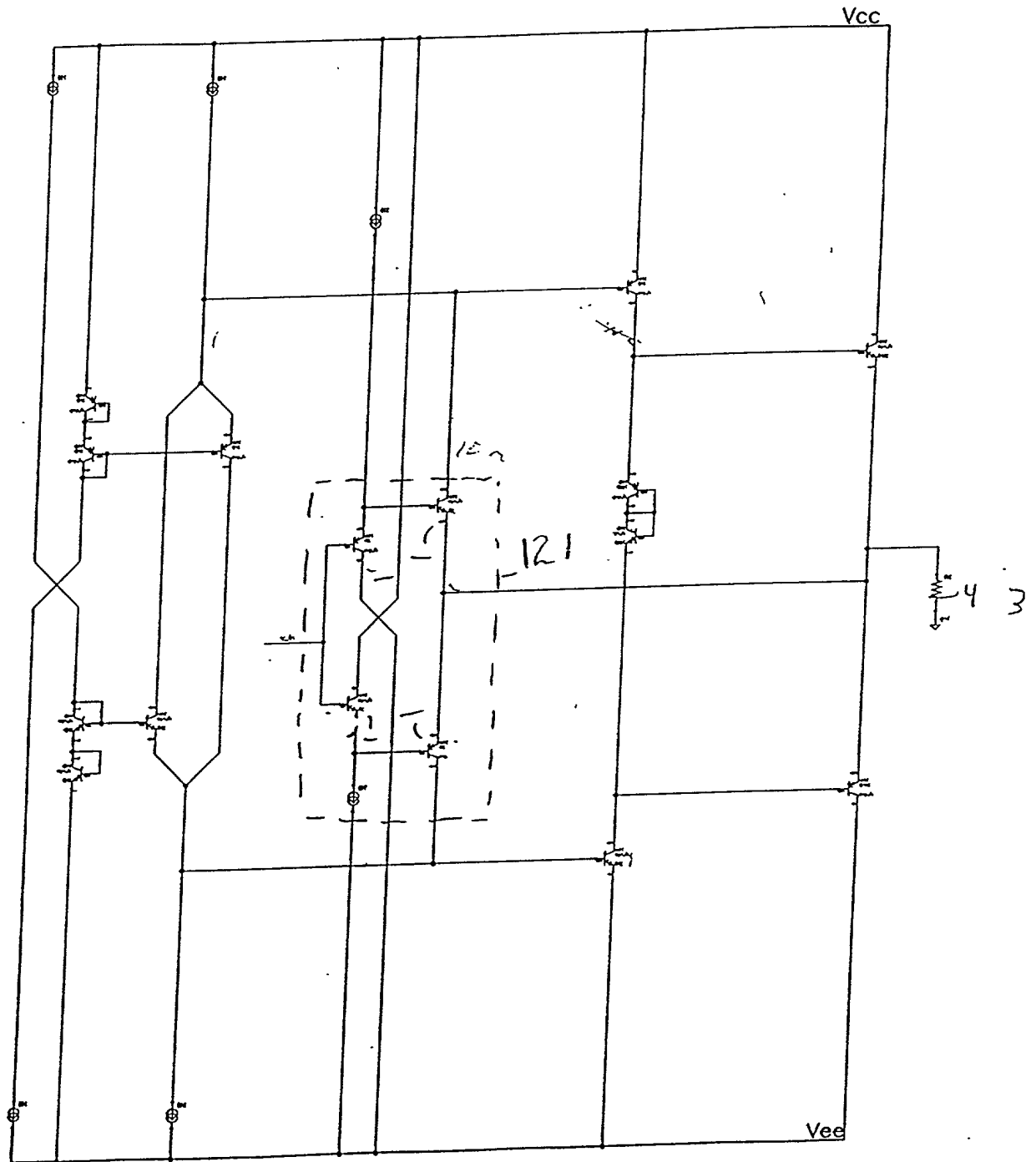


Figure 24

400393-4004

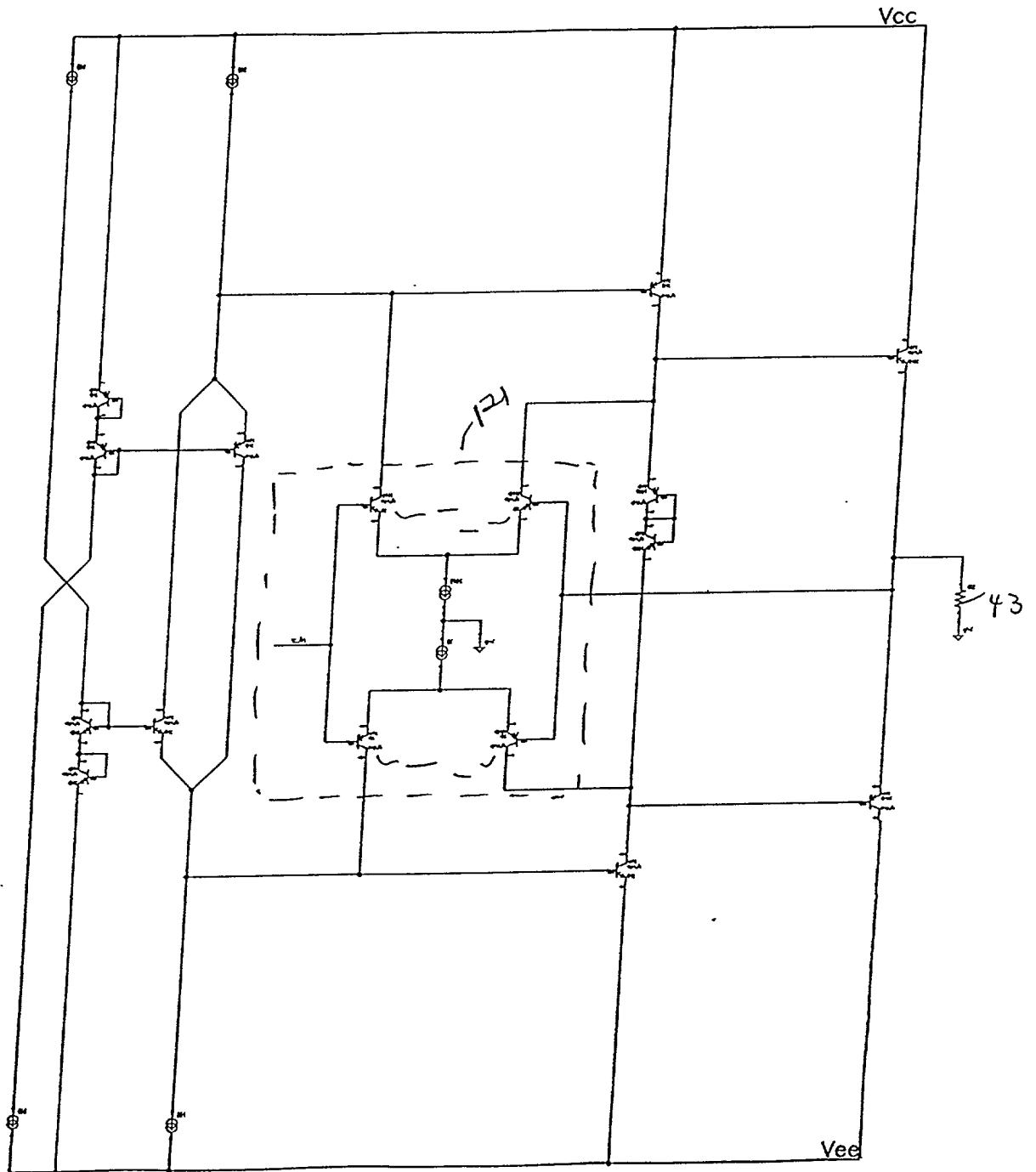


Figure 25

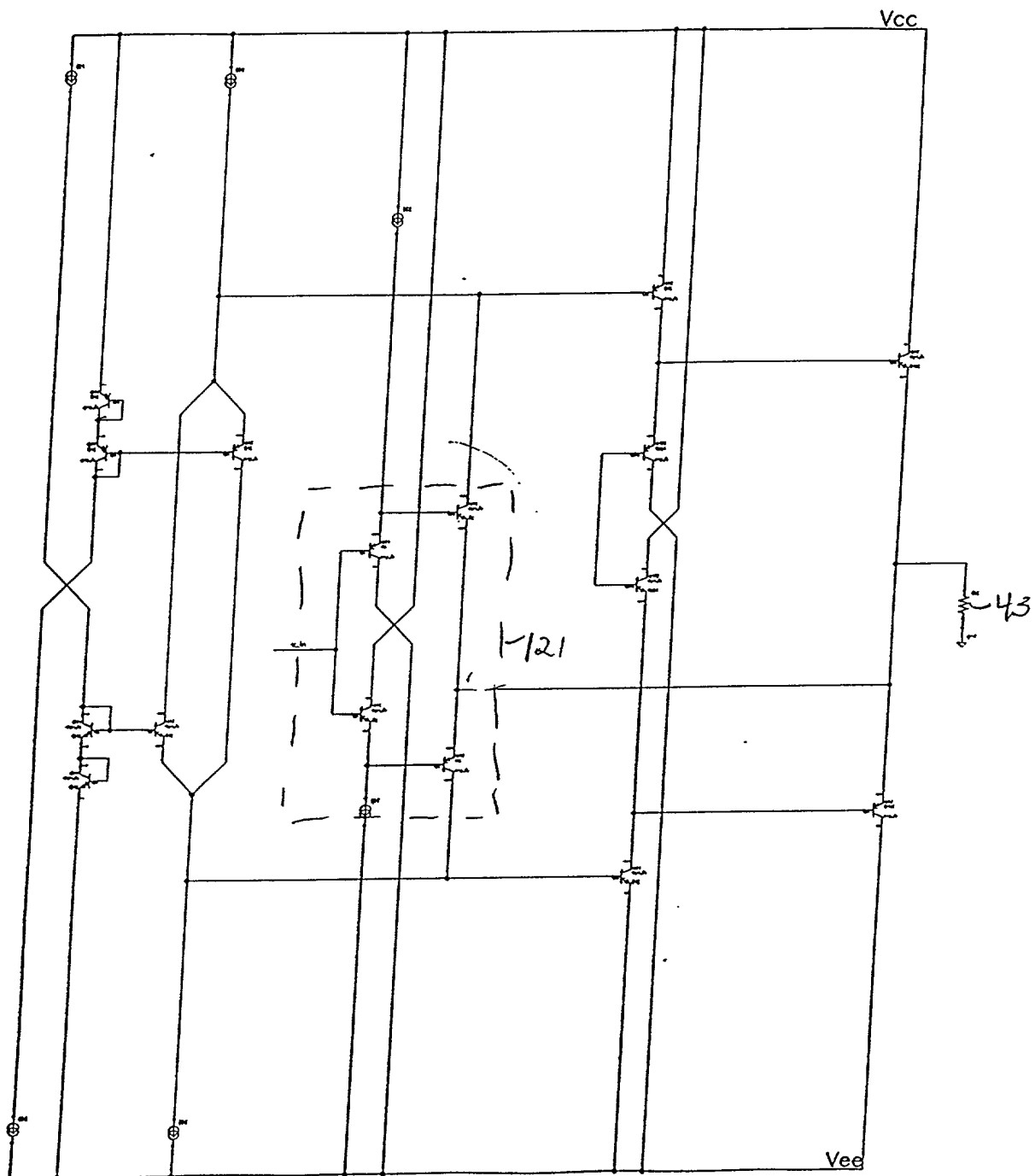
[illegible]

Figure 26

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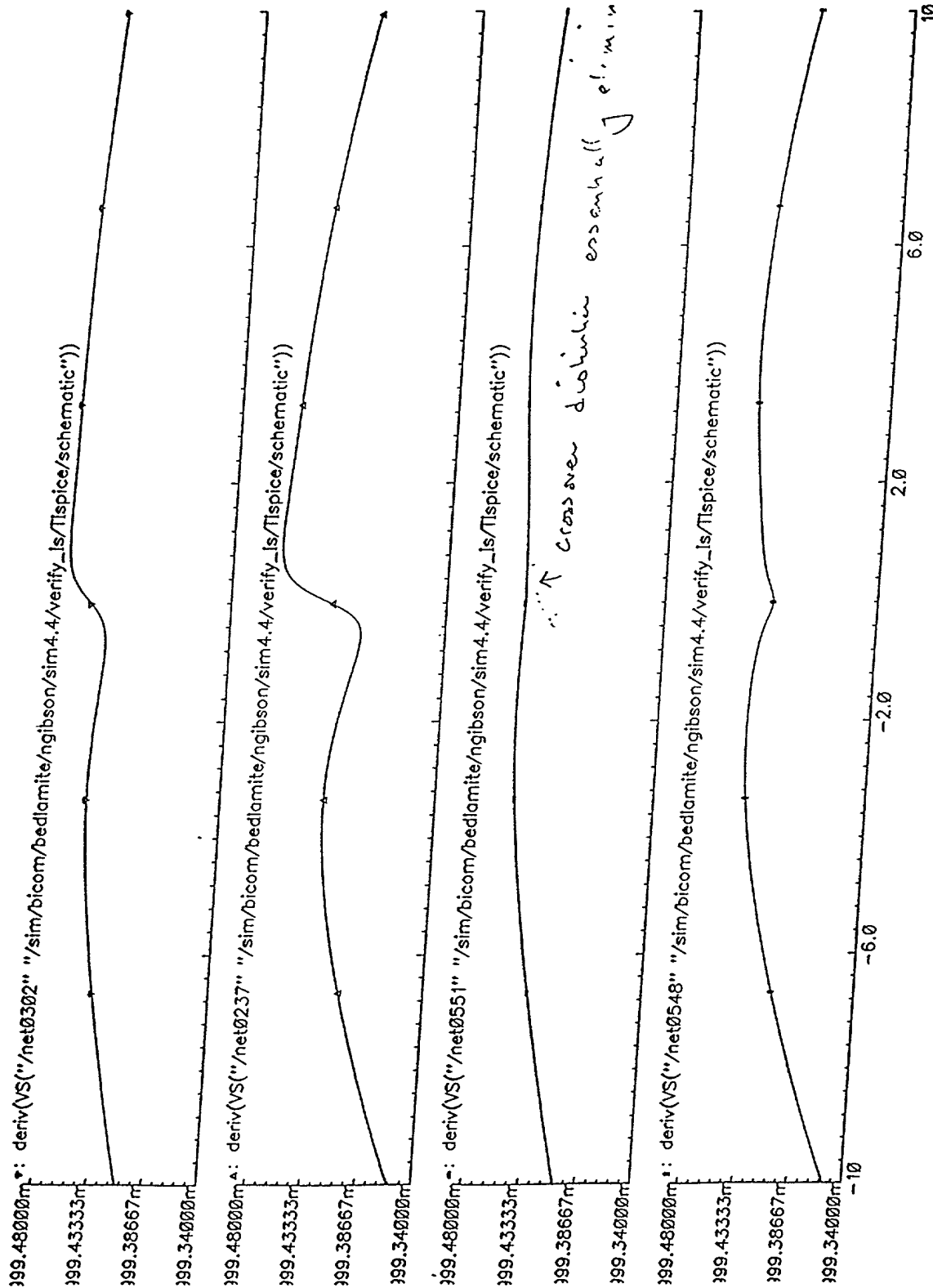


Figure 28

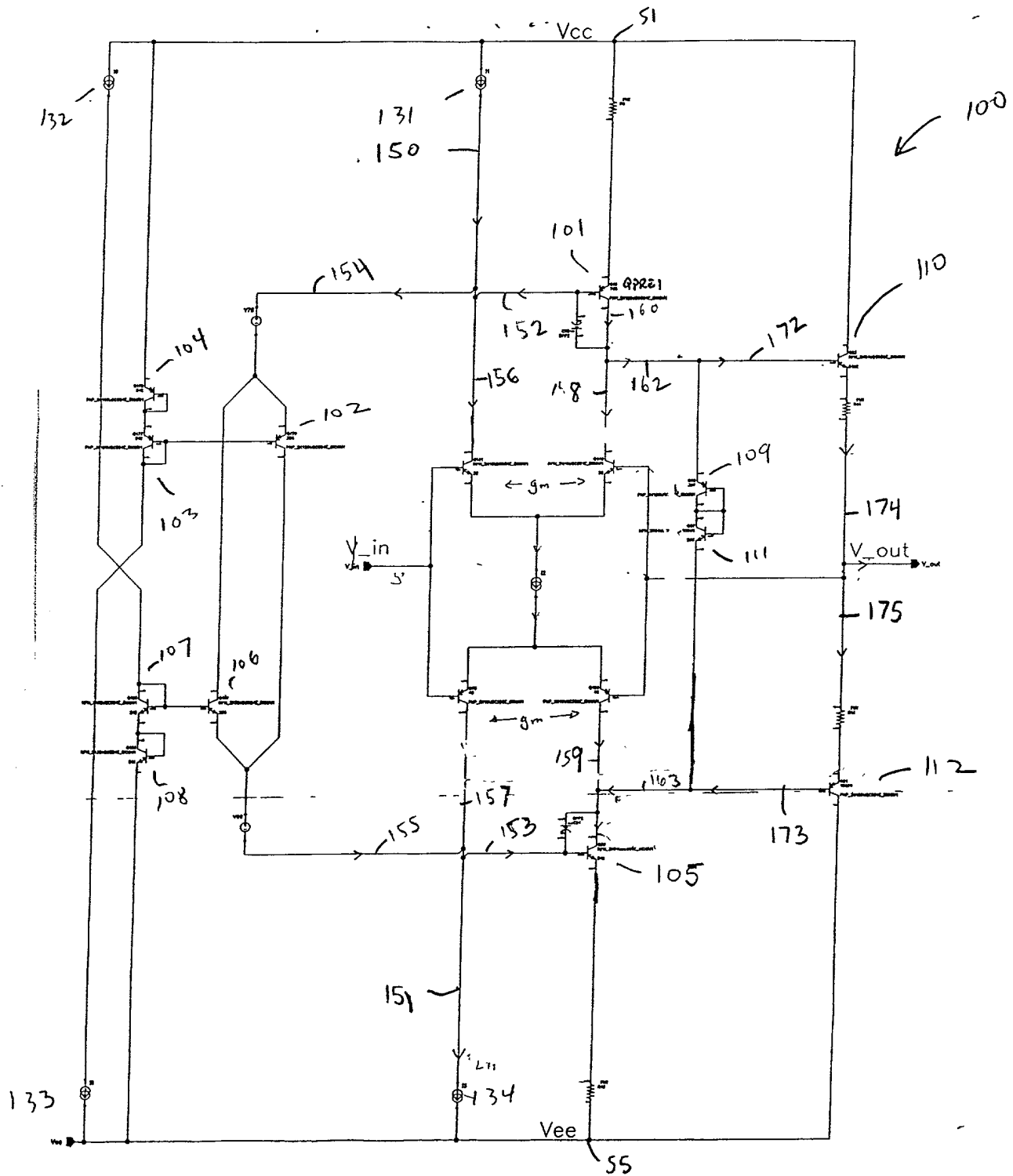


Figure 29

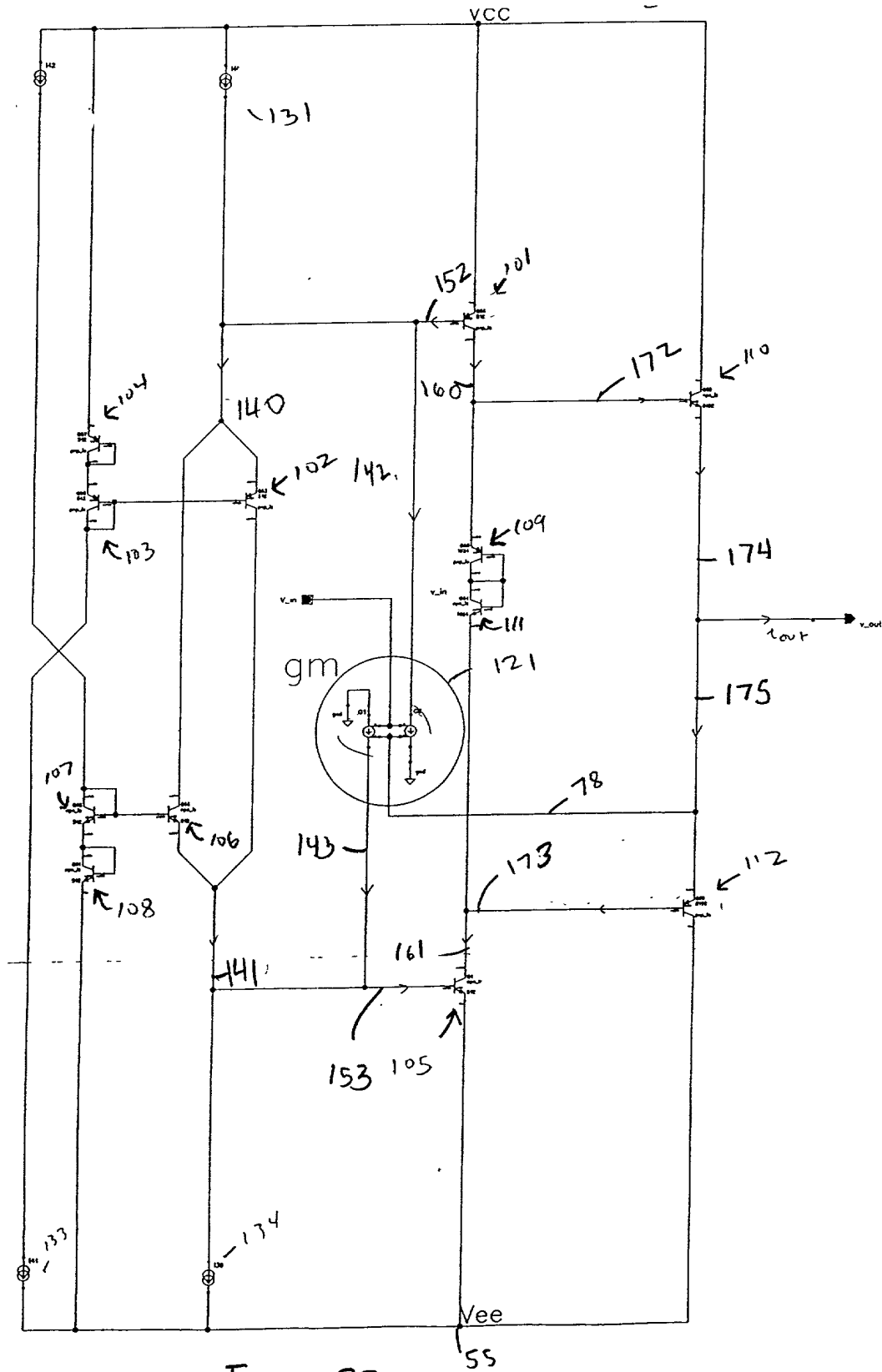


Figure 30

100

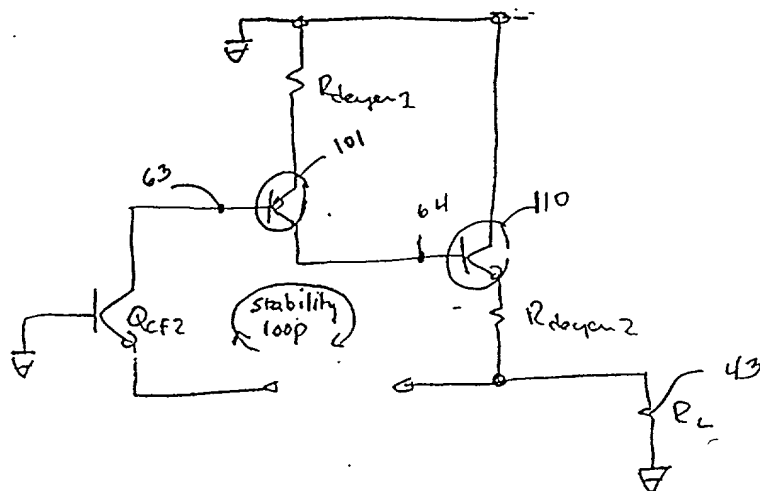


Figure 31

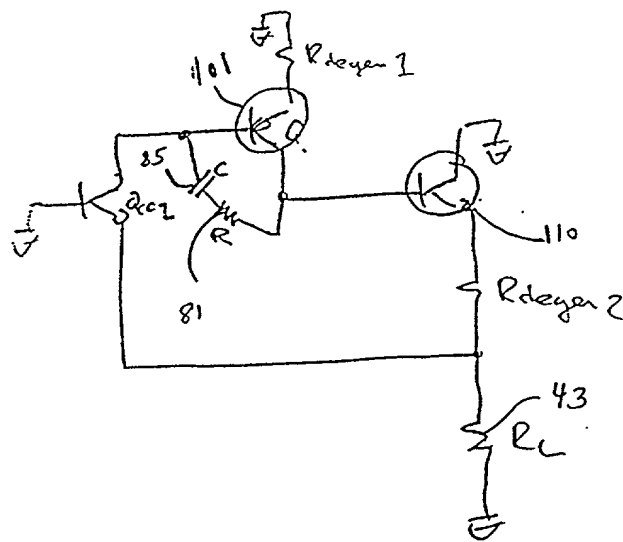


Figure 32

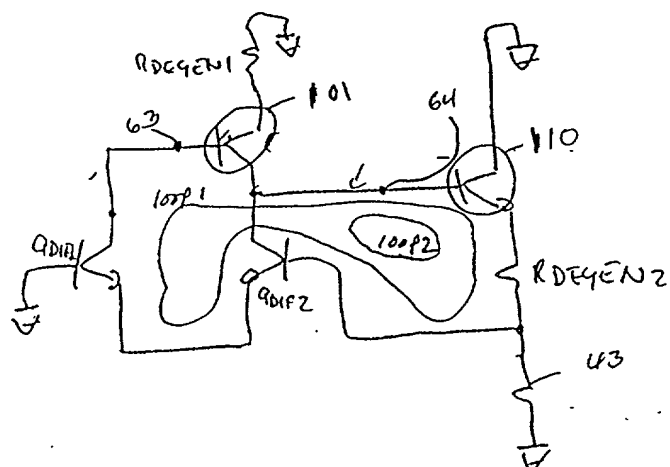


Figure 33

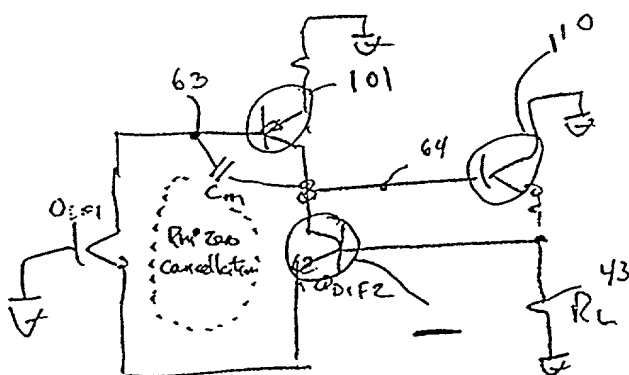


Figure 34

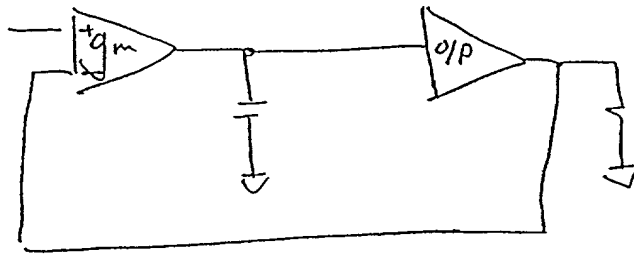


Figure 35